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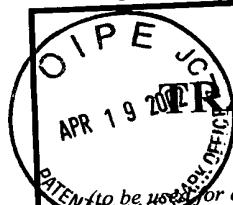
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PTO/SB/21 (6-99)

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Total Number of Pages in This Submission

63

Application Number

09/437,882

Filing Date

November 10, 1999

First Named Inventor

Lex Kosowsky

Group/Art Unit

2841

Examiner Name

I. Patel

Attorney Docket Number

23587-702

ENCLOSURES (check all that apply)

Fee Transmittal Form
 Fee Attached
 Response to Restriction As to Species
 After Final
 Version with Markings Showing Changes
 Affidavits/declaration(s)
 Extension of Time Request
 Information Disclosure Statement & 3 cited references
 Certified Copy of Priority Document(s)
 Response to Missing Parts/ Incomplete Application
 Response to Missing Parts under 37 CFR 1.52 or 1.53

Assignment Papers (for an Application)
 Drawing(s)
 Licensing-related Papers
 Petition Routing Slip (PTO/SB/69) and Accompanying Petition
 Petition to Convert to a Provisional Application
 Power of Attorney, Revocation Change of Correspondence Address
 Terminal Disclaimer
 Small Entity Statement
 Request for Refund

After Allowance Communication to Group
 Appeal Communication to Board of Appeals and Interferences
 Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)
 Proprietary Information
 Status Letter
 Additional Enclosure(s) (please identify below):

Revised Fig. 7 and redlined Fig. 7
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Remarks

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

application of: Lex Kosowsky

Serial No.: 09/437,882

Filed: November 10, 1999

For: CURRENT CARRYING STRUCTURE
USING VOLTAGE SWITCHABLE
DIELECTRIC MATERIAL

Group No.: 2841

Examiner: I. Patel

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AMENDMENTAssistant Commissioner for Patents
Washington, D.C. 20231

Sir:

In response to Examiner's Office Action mailed October 19, 2001, Applicants respectfully request reconsideration of the above-referenced application in view of the following amendments and remarks. Submitted herewith is an amended Figure 7 and a redlined version showing the changes that were made.

AMENDMENTIn the Drawings:

Please replace Figure 7 with new Figure 7.

In the Specification

Please replace the paragraph beginning at page 20, line 17, with the following rewritten paragraph:

--FIG. 3C shows that the non-conductive layer 20 is patterned on the substrate 10. In an embodiment, a mask is applied over the non-conductive layer 20. The mask is used to expose the substrate 10 through the positive photoresist. The pattern of the exposed substrate 10 corresponds to a pattern in which current carrying elements will be formed on the substrate.--

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